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Substitute for form 1449/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  <i>(Use as many sheets as necessary)</i>				<b>Complete If Known</b>	
				Application Number	10/514,429
				Filing Date	July 12, 2005
				First Named Inventor	Robert DWILINSKI
				Art Unit	2881
				Examiner Name	M. J. Logie
Sheet	1	of	1	Attorney Docket Number	204552033800

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number Number-Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
/M.L./	1.	US-6,031,858	02-29-2000	Hatakoshi et al.	
/M.L./	2.	US-2001/0030328-A1	10-18-2001	Ishida	
	3.	US-7,057,211	06-06-2006	Dwilinski et al.	

FOREIGN PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear

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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
/M.L./	1.	Sze, S.M. (1998). "Appendix G: Properties of Important Element and Binary Semiconductors," in <u>Modern Semiconductor Device Physics</u> . John Wiley & Sons, Inc. New York. Esp. pp. 539-540	
/M.L./	2.	M. Fukuda. (1999) <u>Optical Semiconductor Devices</u> ; Wiley Series in Microwave and Optical Engineering. Chang, K., John Wiley & Sons, Inc. New York. Esp. pp. 7	
/M.L./	3.	U.S. Office Action, mailed April 15, 2008, directed to related U.S. Patent Application No. 10/493,594. 27 pages	

Examiner Signature	/Michael Logie/	Date Considered	06/09/2008
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